S100 TEST SYSTEM
## THE SUPERLATIVE 100MHZ TESTER

KEY FEATURES

- Cost-optimized
- High pin-count, high speed
- Easy maintenance
- High reliability
- Multi-function board design
- Worldwide installation base

THE MOST COST/PERFORMANCE-OPTIMIZED 100MHZ TEST SOLUTION IN THE INDUSTRY

ABOUT THE S100 FAMILY

Inheriting the V50's outstanding design, the S100 is the new generation of testing platforms, boasting a stellar cost/performance ratio, unmatched reliability and low cost. Based on our customers' needs, the S100 accommodates a wide variety of testing applications, incorporating 2C, 5C, 7D and 15D in a single model. The S100 system is ideally suited for multisite enlargement and high pin-count devices, offering effortless setup and debugging.

- 1536-pin (max) supports more multi-site and higher pin-count devices.
- Cable- or direct-mount ability lets you balance cost, flexibility and signal integrity.
- Instruments to fulfill today's complex consumer IC test requirements: LOG, MIX, MIP and more.
- Lightweight, easy to install and maintain with small footprint and considerate backplane.
- Flexible, slot-based architecture means you only configure what you need, every time!

YTEC S100 — 2C, 5C, 7D-CP, 15D-CP, 7D-FT, 15D-FT
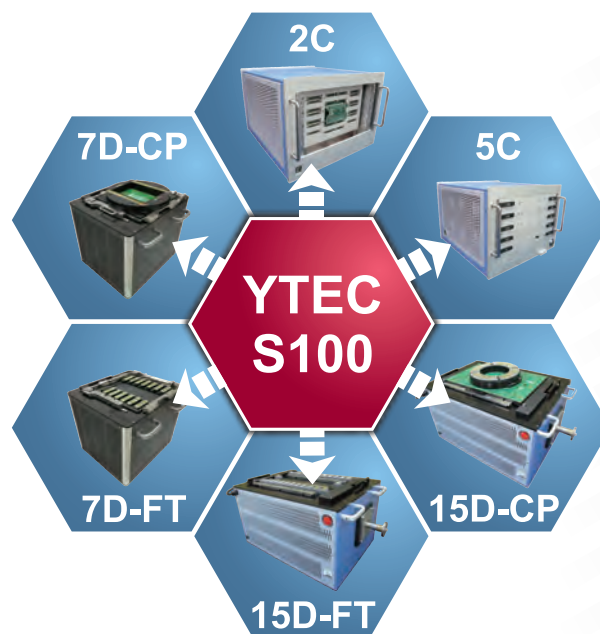
	2C	5C	7D	15D
CABLE MOUNT	■	■		
DIRECT MOUNT			■	■
MAX CHANNELS	256	512	768	1536
FUNCTION BOARD SLOTS	4	4	6	12
2C: V50/S50 COMPATIBLE				

YOUNGTEK ELECTRONICS CORP.

YTEC

POWERFUL SUITE OF SOFTWARE TOOLS
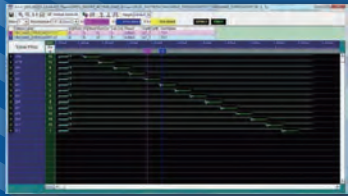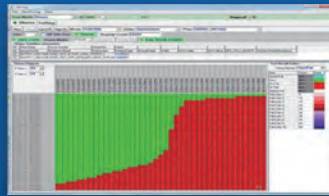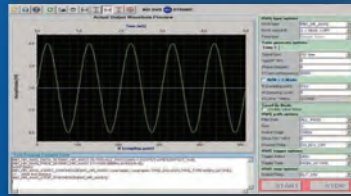
LA TOOL   **SHMOO TOOL**   **AWG TOOL**   **SCOPE TOOL**
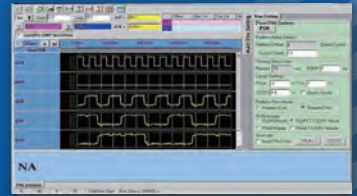
SPECIFICATIONS

MAX PIN COUNT	1536
MAX MULTI SITE TEST	256
MAX TEST RATE	100MHZ
PATTERN MEMORY	32M(LOG), 128M(LOG1)
LOG BD	128 PE CH / 8 DPS / 16 PMU / 4 GPMU / 8 TMU
	FRC(LOG1) / ALPG(LOG1)
MIP BD	16 PE CH / 16 MCPMU / 8 TMU / 8 FTMU
MIX BD	2 CH (HR AWG) / 2 CH (HS AWG) /
	2 CH (HR DIGITIZER) / 2 CH (HS DIGITIZER) /
	32 BIT DAQ
DEVELOP ENVIRONMENT	WIN7 / WIN10, C/C++
USER POWER ON/OFF	SOFTWARE-CONTROLLED
SYNCH. MECHANISM	LOG/LOG1/MIP BOARD@ SLOT1 AS MASTER
CABLE MOUNT SOLUTION	YES (2C/5C)
DIRECT MOUNT SOLUTION	YES (7D/15D)
USER RELAY	96 (5C/7D)
	128 (2C/15D)
USER POWER	+3.3V, 5V, +/-15V
POWER CONSUMPTION	1700 WATTS (2C/5C)
	2500 WATTS (7D)
	5000 WATTS (15D)
DIMENSIONS	L:51.5CM, W:48.0CM, H:35.4CM (2C/5C)
	L:55.0CM, W:47.0CM, H:44.95CM (7D)
	L:82.0CM, W:57.0CM, H:48.0CM (15D)
WEIGHT	75KG (2C/5C) / 85KG (7D) / 170KG (15D)

APPLICATIONS

DIGITAL ICS	SSD CONTROLLER IC, USB 3.0 IC, etc.
ANALOG ICS	VOLTAGE REFERENCES, POWER IC, etc.
MIXED-SIGNAL ICS	TOUCH IC, FINGER PRINT IC, etc.
OTHER ICS	LED DRIVER IC, MEMS IC, etc.

LOG BD

PE CH	128
TEST RATE	100MHZ
VIL/VIH	-2 ~ +7V
VOL/VOH	-2 ~ +7V
ACTIVE LOAD	±24 MA
NUMBER OF EDGES	4
EPA	±500 PS
PATTERN MEMORY	32M(LOG), 128M(LOG1)
SCAN CHAIN	1G (LOG)
	4G (LOG1)
TMU RANGE	MAX 300MHZ
DPS	-1V TO 15V
DPS MAX OUTPUT	0.7A (GANG UP TO 2.8A)
PMU	±10 V, ±500 MA
GPMU	±10 V, ±500 MA
PPMU	-2V~11.8V(LOG),7.0V(LOG1);±32MA
DVM	4 CH, ±10V

MVI BD

MCPMU	±40 V, ±2A
HVPMU	0~80V (FLOATING GND), ±1A
DVM	4 SET, ±40V (COMMON MODE) / ±10V (DIFFERENTIAL MODE)
TMU	8 SET, SAMPLE RATE 200MHZ
FTMU	8 SET, SAMPLE RATE 6.4GHZ
PE CH	16
TEST RATE	100MHZ
VIL/VIH	-2 ~ +7V
VOL/VOH	-2 ~ +7V / -20 ~ +70V
ACTIVE LOAD	±24 MA
NUMBER OF EDGES	4
EPA	±500 PS
PATTERN MEMORY	128M

MIX BD

	HIGH RES. AWG	HIGH SPEED AWG
# OF AWG	2 PER INSTRUMENT	2 PER INSTRUMENT
SAMPLE RATE	1MSPS	250MSPS
RESOLUTION	16 BIT	16 BIT
OUTPUT VOLTAGE	±8 V	±3 V
OUTPUT SETTING	SINGLE END / DIFF.	SINGLE END / DIFF.
MEMORY DEPTH	256K	256K
	HIGH RES. DIGITIZER	HIGH SPEED DIGITIZER
# OF DIGITIZER	2 PER INSTRUMENT	2 PER INSTRUMENT
SAMPLE RATE	1MSPS	100MSPS
RESOLUTION	18 BIT	16 BIT
INPUT VOLTAGE	±8 V	±3.3 V
INPUT SETTING	SINGLE END / DIFF.	SINGLE END / DIFF.
MEMORY DEPTH	256K	256K
DAQ	32 BIT, 100MHZ	
VOLTAGE SOURCE	8 CH, ±8 V, ±50MA	
DVM	4 CH, ±10V	

ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE

YTEC

PHONE: + 886 3-666-9968   WEB: WWW.YTEC.COM.TW